

Model : GP605

GP555

Ref. No. : FF-T01-K1-000062-01

Date : September 12, 2000

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**Location:** Image Processor PCB Assembly / ADF Controller PCB Assembly

**Subject:** Updating the ROMs for the Image Processor PCB Assembly  
/ ADF Controller PCB Assembly (IP Ver. 33.01 / ADF-Y Ver. 10.01)

**Reason:** In the existing version, the ADF wrongly detects the size of a Z-fold original (initial identification), causing a jam in the feeder.  
To correct the fault, the ROM on the image processor PCB / ADF controller PCB has been modified.  
The implementation of this modification requires simultaneous replacement of the ROMs.

[History of ROM Modifications] (machine side)

| Serv. Info. No.     | Ver. No.   |            | Description   | Date               |
|---------------------|------------|------------|---|--------------------|
|                     | IP         | MFC        |   |                    |
| FF-T01-K1-000062-01 | Ver. 33.01 | Ver. 30.01 | IP: Correction of wrong detection of Z-fold original                                    | September 12, 2000 |
| FF-T01-K1-000043-01 | Ver. 32.01 | Ver. 30.01 | IP: Correction of malfunction   | March 15, 2000     |
|                     | Ver. 30.01 | Ver. 30.01 | IP: Software new compression chip-enabled<br>MCF: Software new compression chip-enabled |                    |
| FF-T01-K1-000031-01 | Ver. 21.01 | Ver. 21.02 | IP: Correction of malfunction<br>MFC: Change to printer compatibility specifications    | August 26, 1999    |
|                     | Ver. 20.11 | Ver. 20.05 | Full system finisher compatibility  |                    |
| FF-T01-K1-000022-02 | Ver. 10.12 | Ver. 10.10 | IP : For compatibility with LIPS IV   | July 1, 1999       |
| FF-T01-K1-000005-01 | Ver. 7.03  | Ver. 7.03  | IP : For compatibility with PDL kanji<br>MFC : Correction of faults                     | March 18, 1999     |
| _____               | Ver. 4.06  | Ver. 6.02  | MFC : Correction of faults  | _____              |
|                     |            | Ver. 5.01  | Early stage production version  | _____              |

**Details:**

&lt;Modifications in IP / ADF Controller &gt;

## • Modifications in IP Ver. 33.01 / ADF-Y Ver. 10.01

| No. | Item  | Description  |
|-----|---|--|
| 1   | Correction of wrong detection of Z-fold original by ADF | <p>When a Z-fold original is used, the machine tends to assume the absence of an original if the original moves over the original trailing edge sensor of the tray with a gap, causing the machine to use stream reading with reference to the small-size original position (while a Z-fold original is a large-size original).</p> <p>This fact then causes the machine to assume the presence of a jam usually caused by a reading error, consequently forcing the reading operation to end. The new version enables normal reading in the presence of these conditions.</p> |

## - Additional Information -

[Modifications Limited to the ADF Controller PCB]

## Preventing Large-Size Original Jams

At times, a jam (0081/0082) can occur because of electric noise while a large-size original is moving past. A jumper wire will be added to ensure proper grounding, and the resistance has been changed.

(The PCB for the ADF controller PCB equipped with measures against large-size original jams is identified by revision number 11.)

**Service Parts:**

| No. | Description |   | Part number  | Q'ty | Stock | Inter-change-ability | PC.<br>-----<br>Stock date |
|-----|-------------|---|--------------|------|-------|----------------------|----------------------------|
| 1   | Old         | EP-ROM  | FF3-3356-090 | 1→0  | C     |                      | P90                        |
|     | New         | EP-ROM  | FF3-3356-100 | 0→1  | D     |                      | *                          |
| 2   | Old         | DIMM ASS'Y  | FG6-1922-210 | 1→0  | C     |                      | P950-2                     |
|     | New         | DIMM ASS'Y  | FG6-1922-330 | 0→1  | D     |                      | *                          |
| 3   | Old         | ADF CONTROLLER PCB ASSEMBLY                         | FG2-9358-080 | 1→0  | C     |                      | P90                        |
|     | New         | ADF CONTROLLER PCB ASSEMBLY                         | FG2-9358-110 | 0→1  | D     |                      | *                          |
| 4   | Old         | IMAGE PROCESSOR PCB ASSEMBLY (for the iR600, GP600) | FG6-1911-060 | 1→0  | C     |                      | P950                       |
|     | New         | IMAGE PROCESSOR PCB ASSEMBLY (GP600)                | FG6-1911-080 | 0→1  | D     |                      | *                          |
| 5   | Old         | IMAGE PROCESSOR PCB ASSEMBLY (GP555)                | FG6-5323-040 | 1→0  | C     |                      | P950                       |
|     | New         | IMAGE PROCESSOR PCB ASSEMBLY (GP555)                | FG6-5323-060 | 0→1  | D     |                      | *                          |

\*1: Interchangeable by replacing all two parts at the same time.

\*2: The ADF controller PCB assembly and the image processor PCB assembly of appropriate models must be replaced at the same time.

\* To be communicated as soon as identified.

**Affected machines:**

[IP Ver. 33.01 / ADF-Y Ver. 10.01]

GP605 120V F13-5631: NLE16091 and later  
 230V F13-5641: PLP00772 and later  
 230V F13-5661: RLE00530 and later